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UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Haltmeyer
Application No. 10/076,948 Group Art Unit: 2182
Filed: 19 February 2002 Examiner: To be assigned
For: THOROUGH OPERATION RESTRICTION

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Technology Center 2100

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INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Sir:

Enclosed are copies of the following reference documents.

<u>Patent No.</u>	<u>Inventor(s)</u>	<u>Date of Publication</u>
US5,802,397	Greenstein et al.	1 September 1998

Summary of the references:

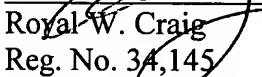
U.S. Patent No. 5,802,397 to Greenstein et al. discloses protection against unwanted I/O accesses in main storage blocks/page frames independent of any storage protection provided against unwanted CPU accesses. An I/O protection array is used containing one-bit I/O keys used only for protecting which respectively protect associated blocks/page frames from unwanted accesses by I/O programs. Each one-bit I/O key is used to allow or disallow I/O

accesses into an associated storage block. Any CPU access is not affected by the I/O storage key. No I/O program key is used and therefore no key compare operation is required. However, the embodiment is compatible with use of CPU storage protection using CPU storage keys in either real form in a CPU storage key array, or in virtual form in page table entries without using any CPU storage key array.

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The above-described reference is submitted herewith along with form PTO-1449 for the convenience of the Examiner. Consideration of the foregoing and the making of the references formally of record in the instant application are respectfully requested.

Respectfully submitted,



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5/7/02
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